

FIG. 1

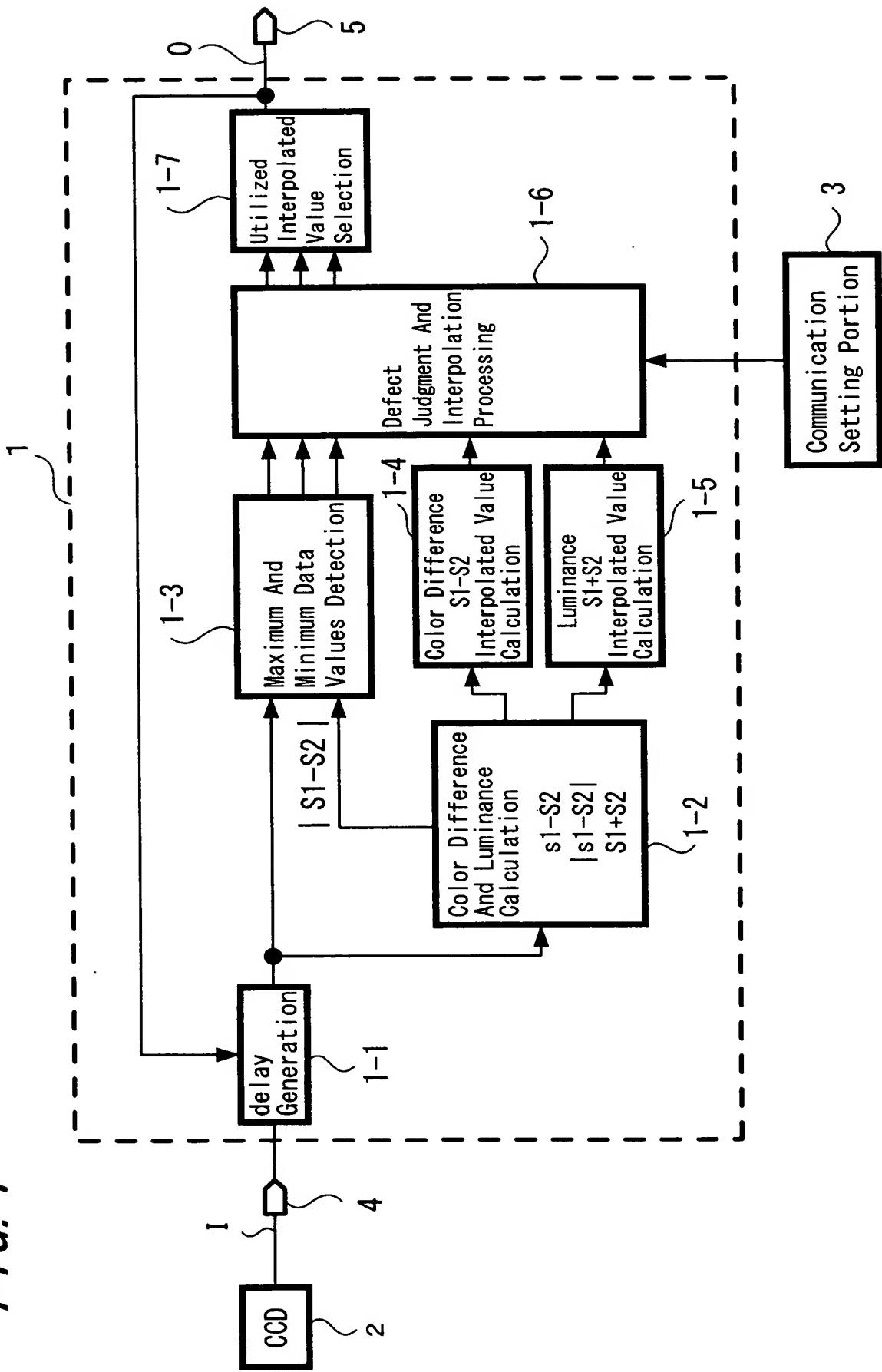


FIG. 2A

	s1	s2	s1	s2	s1	s2	
xIn	A	A'	B	B'	C	C'	
xDL1							
xDL2	D	D'	E	E'	F	F'	
xDL3							
xDL4	G	G'	H	H'	I	I'	

FIG. 2B

	s1	s2	s1	s2	s1	s2	
xIn	A		B		C		
xDL1							
xDL2	D	D'	E	E'	F	F'	
xDL3							
xDL4	G		H		I		

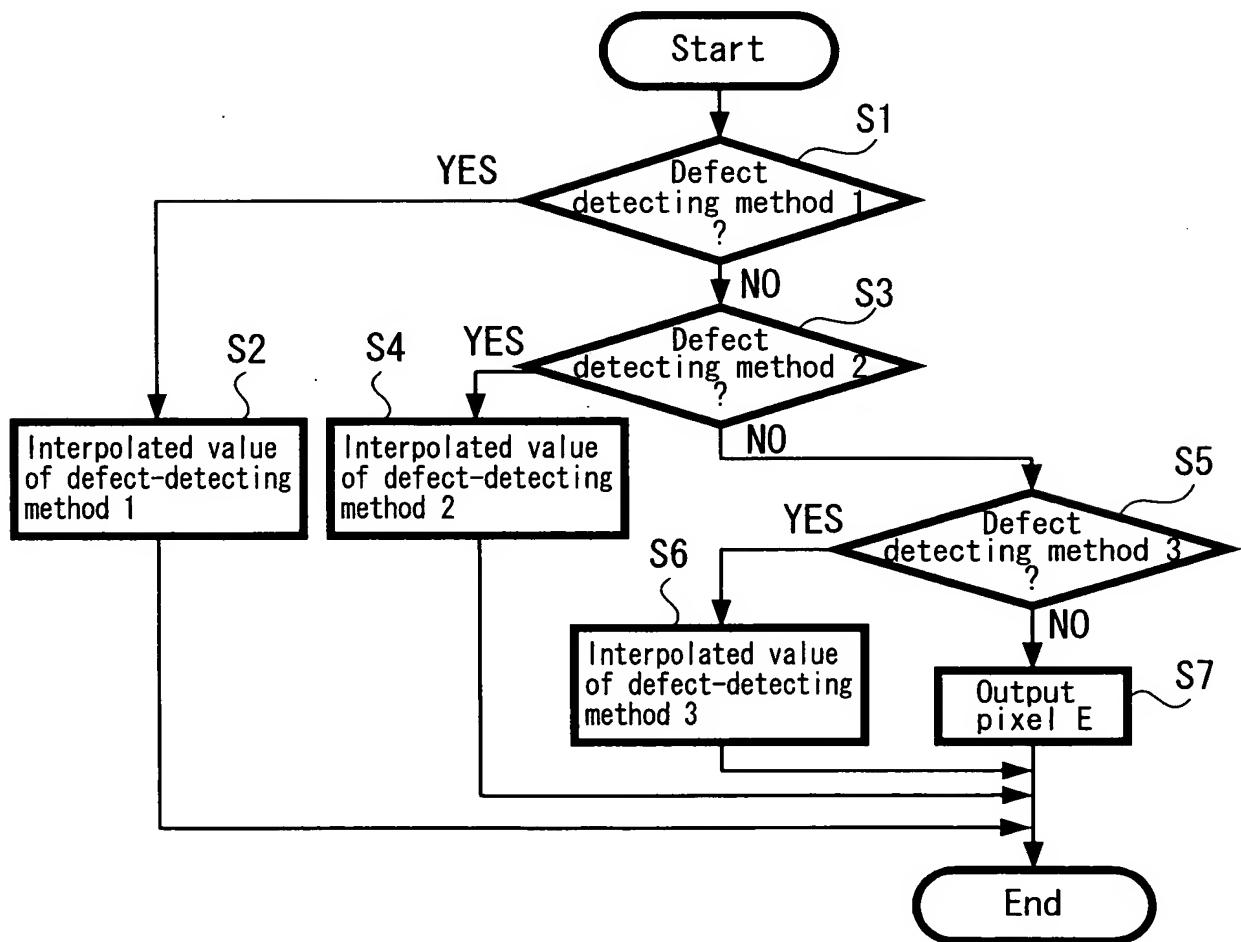
FIG. 2C

	s1	s2	s1	s2	s1	s2	
xIn							
xDL1	A''	B''	C''				
xDL2	D''	E	F''				
xDL3	G''	H''	I''				
xDL4							

FIG. 3

	s1	s2	s1	s2	s1	s2
xIn			B	B'		
xDL1						
xDL2	D	D'	E	E'	F	F'
xDL3						
xDL4			H	H'		

FIG. 4



DESCRIPTION OF REFERENCE NUMERALS

- 1 ... DEFECT DETECTION AND CORRECTION CONTROLLING BLOCK
- 1-1 ... DELAY GENERATING BLOCK
- 1-2 ... COLOR DIFFERENCE AND LUMINANCE CALCULATING BLOCK
- 1-3 ... MAXIMUM AND MINIMUM DATA VALUES DETECTING BLOCK
- 1-4 ... COLOR DIFFERENCE INTERPOLATED VALUE CALCULATING BLOCK
- 1-5 ... LUMINANCE INTERPOLATED VALUE CALCULATING BLOCK
- 1-6 ... DEFECT JUDGMENT AND INTERPOLATION PROCESSING BLOCK
- 1-7 ... INTERPOLATED VALUE FOR USE SELECTING BLOCK
- 2 ... CCD
- 3 ... COMMUNICATION SETTING PORTION
- 4 ... INPUT TERMINAL
- 5 ... OUTPUT TERMINAL
- I ... IMAGE INPUT SIGNAL INCLUDING PIXEL DEFECTS
- O ... IMAGE OUTPUT SIGNAL INCLUDING DATA CORRECTED